

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Suaris *et al.*

Date Filed January 11, 2008

Patent No. 7,251,803

Issued: July 31, 2007

Application No. 10/785,608

Filed: February 23, 2004

Confirmation No. 3195

For: MEMORY RE- IMPLEMENTATION
FOR FIELD PROGRAMMABLE GATE
ARRAYS

Examiner: Stacy Whitmore

Art Unit: 2813

Attorney Reference No. 1011-67730-01

Filed via EFS January 11, 2008.

REQUEST FOR CERTIFICATE OF CORRECTION

The following printing errors were noted in comparing the printed patent with the papers in the attorneys' files:

In the Specification:

Column 1, line 52, "modem" should read -- modern --

Column 5, line 42, "implementation" should read -- implementation. --

Column 8, line 12, "Part 1.A" should read -- Part 1. A --

Column 13, line 6, "which well known" should read -- which are well known --

Column 13, line 43, "and 20 movement" should read -- and movement --

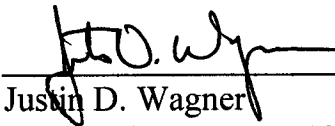
All of the above errors are attributable to the Patent Office, and a Certificate of Correction is enclosed to make formal notice of the errors in the referenced patent.

Respectfully submitted,

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,251,803

DATED : July 31, 2007

INVENTOR(S) : Suaris et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification:

Column 1, line 52, "modem" should read -- modern --

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